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REPLY UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2183

PATENT  
5500-88700/TT5374

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/726,902

Filed: December 3, 2003

Inventors:

Mitchell Alsup

Gregory William Smaus

Title: Transitioning From  
Instruction Cache to Trace  
Cache on Label Boundaries

§ Examiner: Fennema, Robert E  
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§ Group/Art Unit: 2183  
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§ Atty. Dkt. No: 5500-88700  
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I hereby certify that this correspondence is being facsimile transmitted to the U.S. Patent and Trademark Office (Fax No. (571) 273-8300), on the date indicated below.

Robert C. Kowert

Printed Name



Signature

October 23, 2006

Date

RESPONSE TO FINAL ACTION OF AUGUST 23, 2006

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Final Action of August 23, 2006, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.